

REMARKS

Claims 1-16 are pending in this application. Claims 6 and 13-16 have been amended by the present Amendment.

While Applicant does not agree with the Examiner's remarks in the June 20, 2007 Advisory Action, in order to advance prosecution of this case, Applicant has amended the claims and files an RCE herewith.

REJECTION UNDER 35 U.S.C. § 103

Reconsideration is respectfully requested of the rejection of claims 6-8 and 13-16 under 35 U.S.C. § 103(b) as being unpatentable over U.S. Patent No. 5,867,690 ("Lee") in view of U.S. Patent Application Pub. No. 2003/0028746 ("Durrant").

Claim 6 recites, *inter alia*, byte-swapping the data read from the at least one peripheral device when the second endian information is different from the first endian information, and, after byte-swapping the data read from the at least one peripheral device, transmitting the byte-swapped data to a system bus of the computer system.

Claims 13 and 16 recite, *inter alia*, byte-swapping the data read from the at least one peripheral device when the second endian information is different from the first endian information, wherein the byte-swapping is performed prior to transmission of the data to a system bus of the computer system.

In contrast to the claimed embodiments, Lee states that the processor 510 reads data from the storage device 520, the data then passes along the system data bus 550 to the byte swapping device 530, wherein after receiving the data from the system data bus 550, the byte swapping device byte swaps the data. See Lee, col.

5, line 63 to col. 6, line 4.

The Examiner maintains that Lee discloses transmitting byte-swapped data to a system bus, and refers to Fig. 5, “depending on the direction of transfer”. See April 3, 2007 Office Action at 2-3. However, Lee discloses that when data is read from the storage device 520, the data is byte-swapped after being transmitted to a system data bus, and when data is written to the storage device 520, the data is byte swapped before being provided to the system data bus. See Lee, col. 5, line 63 to col. 6, line 14.

In contrast to Lee, the claims, as amended, require that when the data is read from the at least peripheral device, byte swapping occurs before transmission to the system data bus.

Further, Durrant does not cure the deficiency in Lee, and, in any event, there is no motivation to modify Lee to byte swap data before transmission to the system data bus during the read operation.

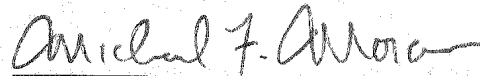
As such, Applicant respectfully submits that claims 6, 13 and 16 are patentable and non-obvious over the cited references.

For at least the reason that claims 7-8 depend from claim 6, and claims 14-15 depend from claim 13, claims 7-8 and 14-15 are also submitted to be patentable and non-obvious over Lee in view Durrant.

Therefore, Applicant respectfully requests that the Examiner withdraw the rejection of claims 6-8, and 13-16 under 35 U.S.C. § 103(a).

An early and favorable reconsideration is earnestly solicited. If the Examiner has any further questions or comments, the Examiner may telephone Applicant's Attorney to reach a prompt disposition of this application.

Respectfully submitted,

A handwritten signature in dark ink, appearing to read "Michael F. Morano", is written over a horizontal line.

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